



# Recent Results on the Development of Ultra Radiation Hard Semiconductor Detectors for Very High Luminosity Colliders

Mara Bruzzi on behalf of the RD50 Collaboration

*INFN, University of Florence, Italy*


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defect engineered Si
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7. Summary

## 1. Motivations

### Large Scale Application of Si Detectors in LHC

*Present working conditions:*

$L \sim 10^{34} \text{ cm}^{-2}\text{s}^{-1}$  in 10 years of LHC operation:

  $\phi \sim 10^{15} \text{ n/cm}^2$  for pixels

$\phi \sim 10^{14} \text{ n/cm}^2$  for microstrips

**LHC upgrade** (“Super-LHC” ... later than 2010)

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$L \sim 10^{35} \text{ cm}^{-2}\text{s}^{-1}$   fluence up to  $10^{16} \text{ cm}^{-2}$  *after five years*

**R&D needed for the development of a detector technology able to operate safely and efficiently in such an environment.**

# Anticipated Radiation Environment for Super LHC

Hadron fluence and radiation dose in different radial layers of the CMS tracker for an integrated luminosity of  $2500\text{fb}^{-1}$ . (CERN-TH/2002-078)

Radius [cm]	Fluence of fast hadrons [ $\text{cm}^{-2}$ ]	Dose [KGy]
4	$1.6 \times 10^{16}$	4200
11	$2.3 \times 10^{15}$	940
22	$8.0 \times 10^{14}$	350
75	$1.5 \times 10^{14}$	35
115	$1.0 \times 10^{14}$	9.3

**The tracker volume can be splitted into 3 radial regions:**

1. **R > 60cm**      **improved Si strip technology**
2. **20cm < R < 60cm**      **improved hybrid pixel technology**
3. **R < 20cm**      **new approaches and concepts required**

## 2. The RD50 CERN Collaboration

### Development of Radiation Hard Semiconductor Devices for High Luminosity Colliders

- Collaboration formed in November 2001 - <http://www.cern.ch/rd50>
- Experiment approved as RD50 by CERN in June 2002
- Main objective:

Development of ultra-radiation hard semiconductor detectors for the luminosity upgrade of the LHC to  $10^{35} \text{ cm}^{-2}\text{s}^{-1}$  (“Super-LHC”).

- Presently 272 Members from 52 Institutes

**Belgium** (Louvain), **Canada** (Montreal), **Czech Republic** (Prague (2x)), **Finland** (Helsinki (2x), Oulu), **Germany** (Berlin, Dortmund, Erfurt, Halle, Hamburg, Karlsruhe), **Greece** (Athens), **Israel** (Tel Aviv), **Italy** (Bari, Bologna, Florence, Milano, Modena, Padova, Perugia, Pisa, Trento, Trieste, Turin), **Lithuania** (Vilnius), **Norway** (Oslo (2x)), **Poland** (Warsaw), **Romania** (Bucharest (2x)), **Russia** (Moscow (2x), St.Petersburg), **Slovenia** (Ljubljana), **Spain** (Barcelona, Valencia), **Sweden** (Lund) **Switzerland** (CERN, PSI), **Ukraine** (Kiev), **United Kingdom** (Exeter, Glasgow, Lancaster, Liverpool, London, Sheffield, University of Surrey), **USA** (Fermilab, Purdue University, Rutgers University, Syracuse University, BNL, University of New Mexico, University of California Santa Cruz)

- **CMS groups in RD50:** Helsinki HIP, Karlsruhe University, Louvain University, INFN Bari-Florence-Perugia-Pisa, Purdue University, PSI-Villigen, Rutgers University..
- **Several RD50 groups are within ATLAS, LHCb, ALICE, CDF and other experiments**

# Scientific strategies

## •Material Engineering

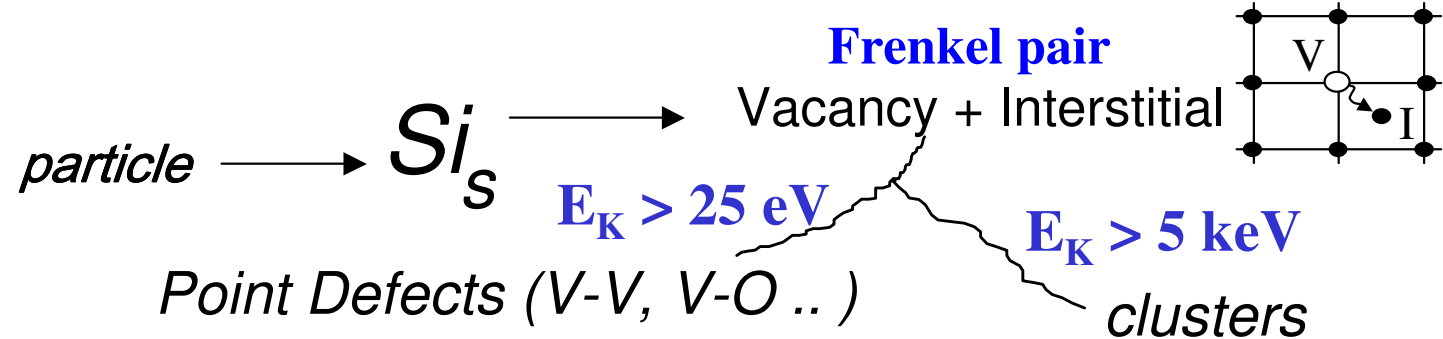
- Defect and Material Characterisation
- Defect Engineering of silicon
- New detector materials (SiC, GaN .. )

## •Device Engineering

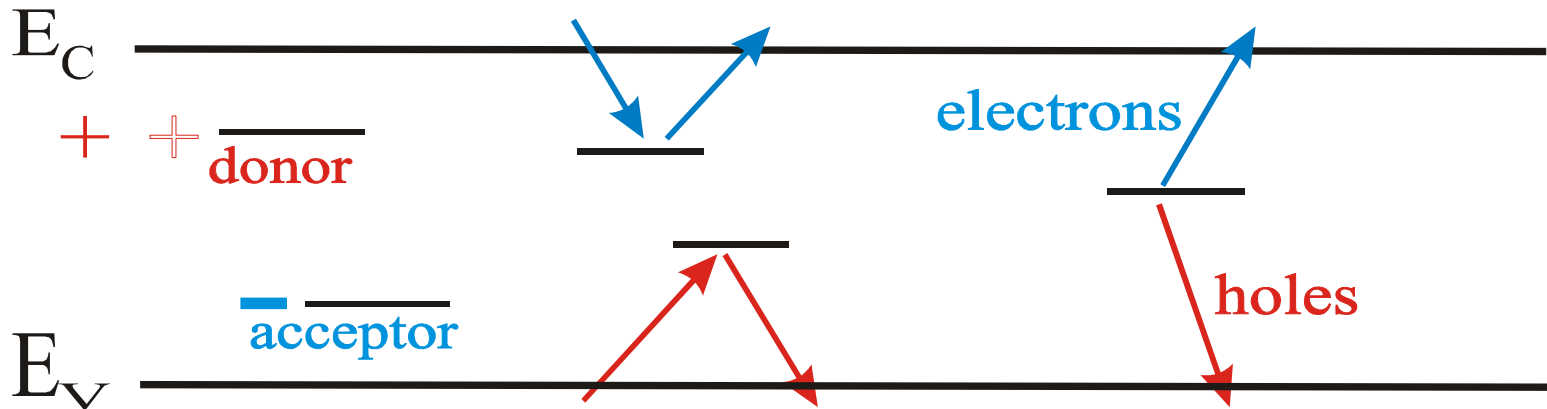
- Improvement of present planar detector structures (3D detectors, thin detectors, semi3-D, stripixel detectors,...)
- Tests of LHC-like detector systems produced with radiation-hard technology
- Variation of the operational conditions

### 3. Radiation effects in silicon detectors

#### Radiation Induced Microscopic Damage in Si



#### Influence of defects on the material and device properties



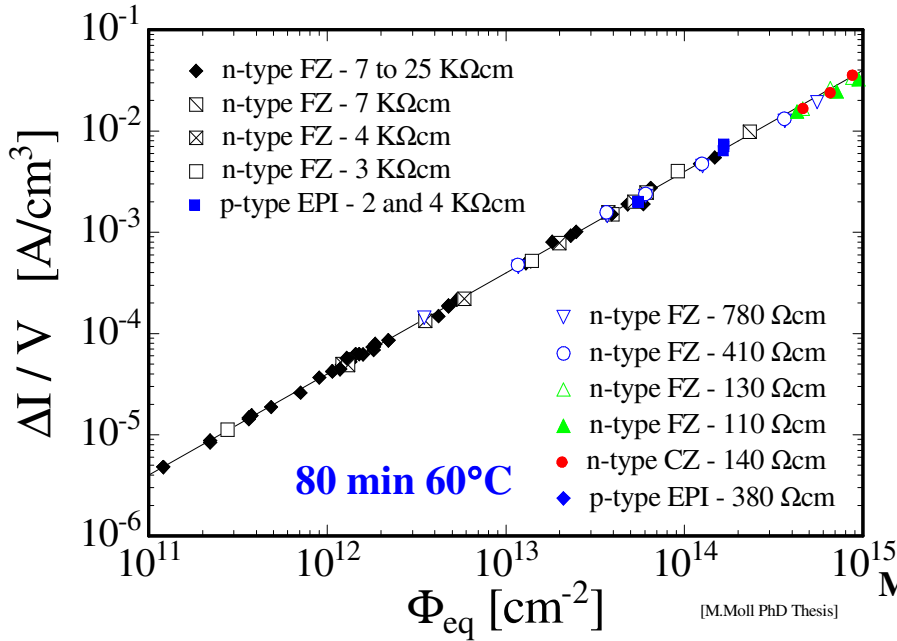
**charged defects**  
 $\Rightarrow N_{\text{eff}}, V_{\text{dep}}$   
 e.g. donors in upper  
 and acceptors in  
 lower half of band  
 gap

**Trapping (e and h)**  
 $\Rightarrow \text{CCE}$   
 shallow defects do not  
 contribute at room  
 temperature due to fast  
 detrapping

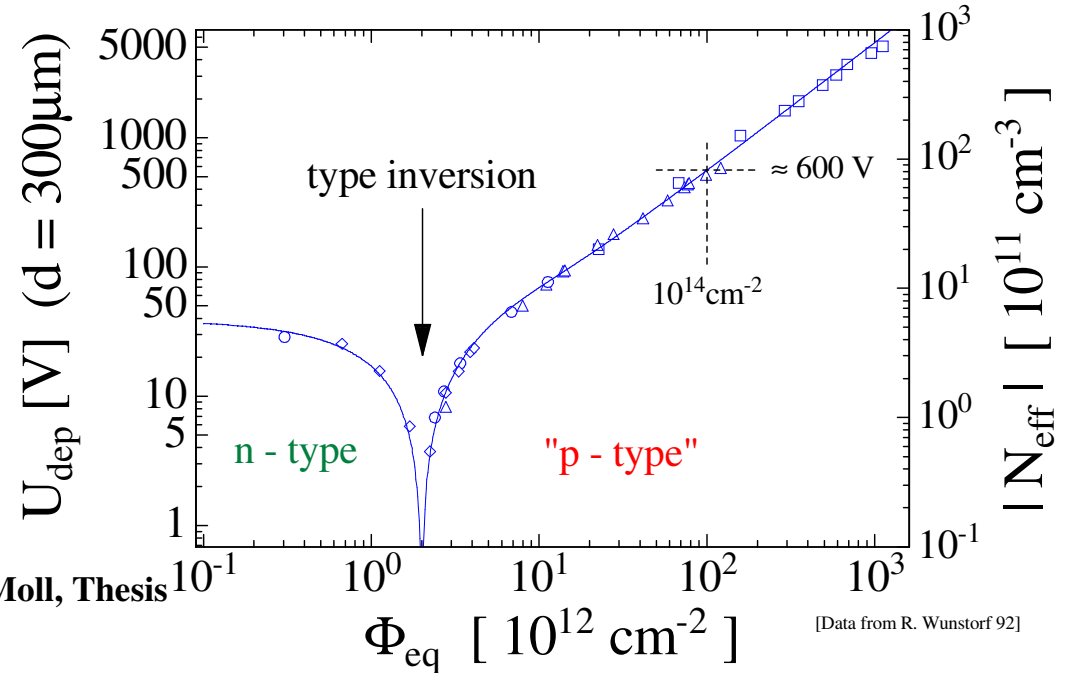
**generation**  
 $\Rightarrow \text{leakage current}$   
 Levels close to  
 midgap  
 most effective

# Macroscopic Radiation Damage in Si

## Leakage Current



## Full Depletion Voltage



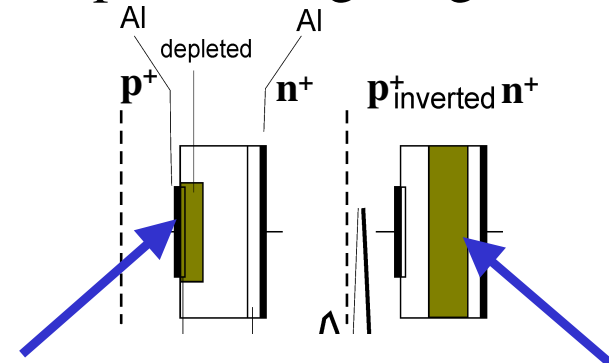
- Damage parameter  $\alpha$  (slope)

$$\alpha = \frac{\Delta I}{V \cdot \Phi_{eq}}$$

- $\alpha$  independent of  $\Phi_{eq}$  and impurities  
 ⇒ used for fluence calibration  
 (NIEL-Hypothesis)

### Type inversion:

SCSI – Space Charge Sign Inversion



before inversion

after inversion

# $V_{dep}$ and $N_{eff}$ depends on storage time and temperature

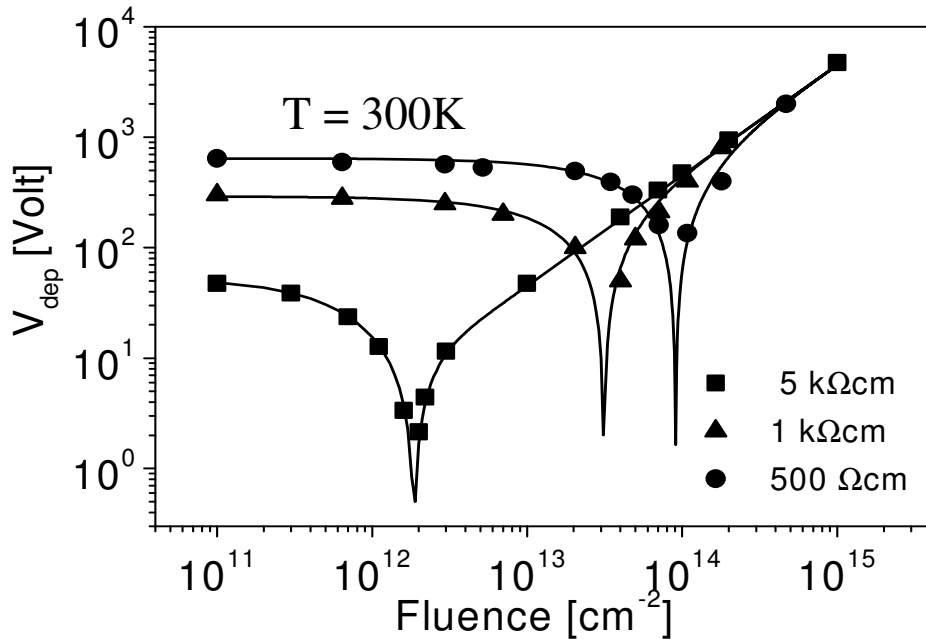
Stable Damage

$$\Delta N_{eff} = N_{C0} (1 - e^{-c \cdot \phi}) + [g_c + g_a e^{-\frac{t}{\tau_a(T)}} + g_y (1 - e^{-\frac{t}{\tau_y(T)}})] \phi$$

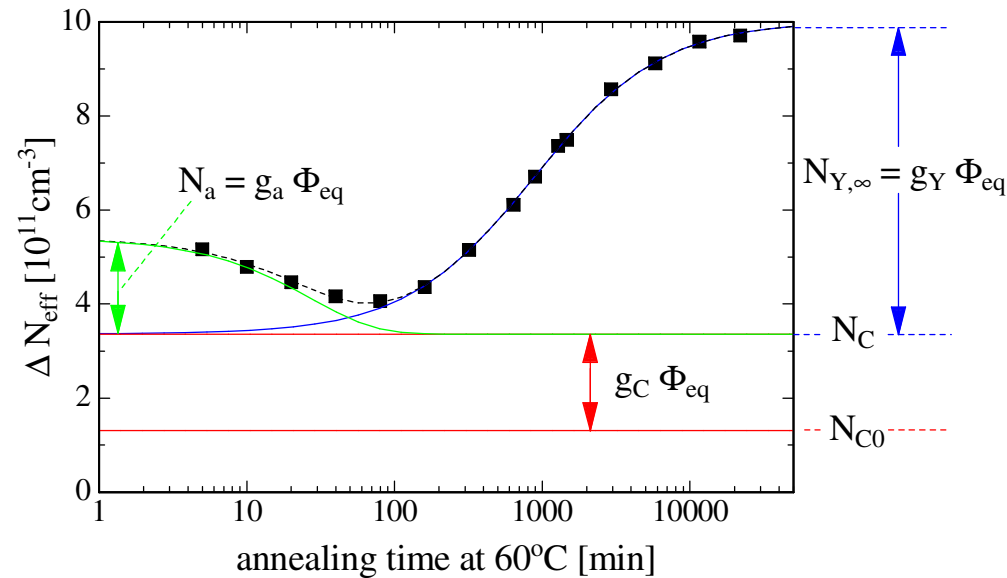
Beneficial Annealing

Reverse Annealing

Shallow Donor Removal



M. Bruzzi, Trans. Nucl. Sci. (2000)



G.Lindstroem et al, NIMA 426 (1999)

- **Short term: “Beneficial annealing”**
- **Long term: “Reverse annealing”**  
**time constant : ~ 500 years (-10°C)**  
**~ 500 days (20°C)**  
**~ 21 hours (60°C)**



# Charge Collection Efficiency

Limited by:

- Partial depletion due to high  $V_{dep}$
- Trapping at deep levels
- Type inversion (SCSI)

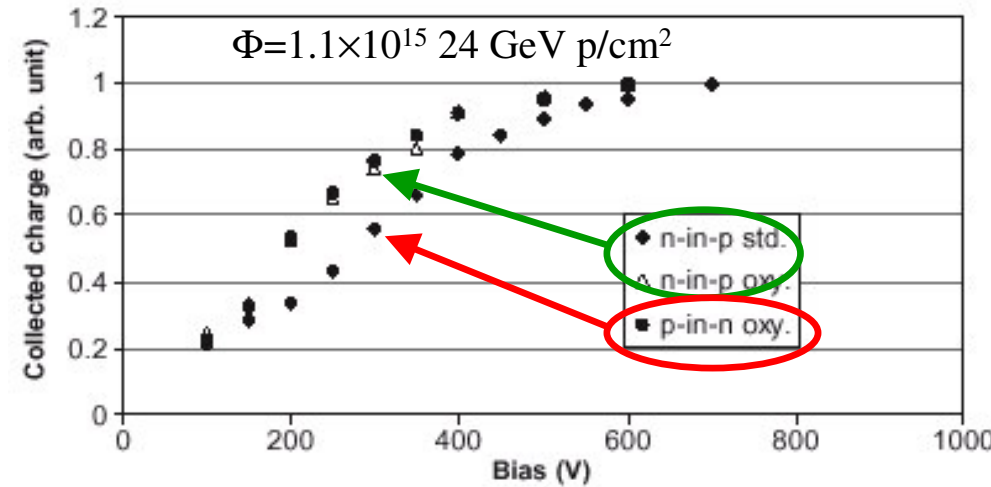
Collected Charge:

$$Q = Q_o \cdot \epsilon_{dep} \cdot \epsilon_{trap}$$

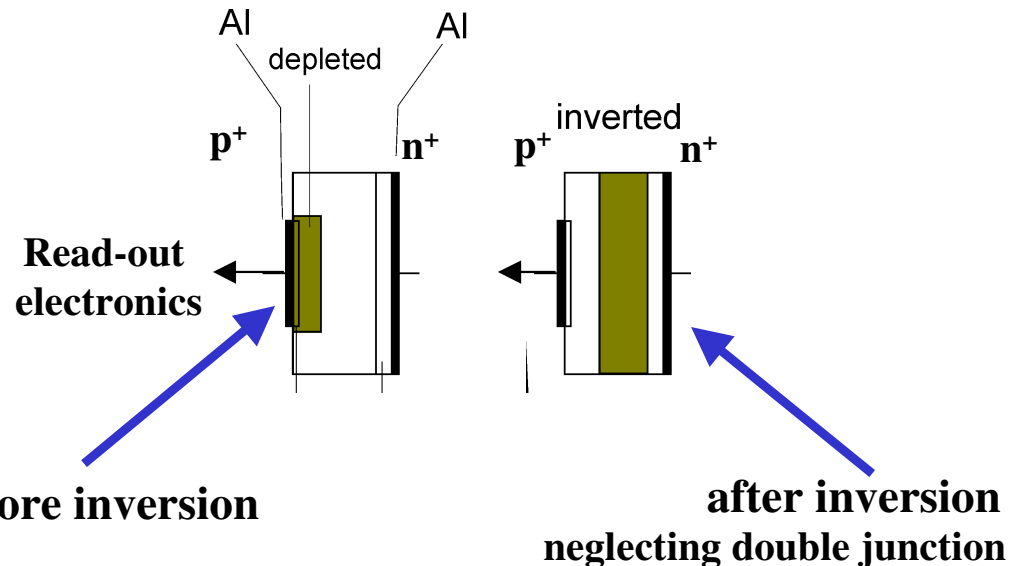
$$\epsilon_{dep} = \frac{d}{W} \quad \epsilon_{trap} = e^{-\frac{\tau_c}{\tau_t}}$$

W: total thickness  
 d: Active thickness  
 $\tau_c$ : Collection time  
 $\tau_t$ : Trapping time

(G. Casse et al., NIM A 485 (2002) 153)



CCE(V) improved if electronic read-out is closest to high electric field: **n<sup>+</sup>-p** detectors (no SCSI) **better than p<sup>+</sup>-n** sensors after SCSI.



## 4. Material Engineering

### Defect Engineering of Silicon

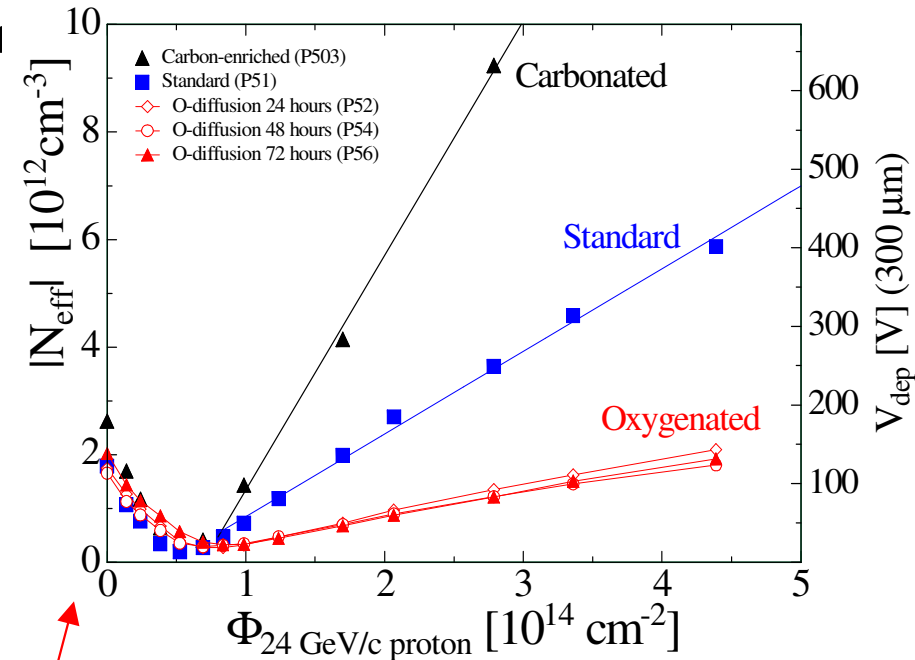
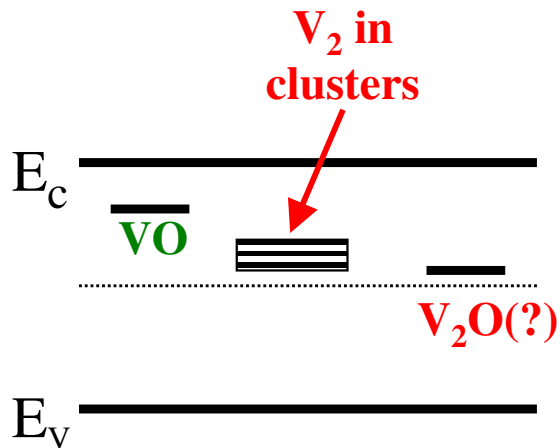
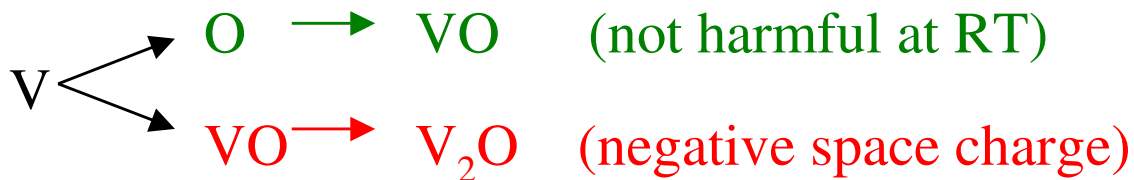
Influence the defect kinetics by incorporation of impurities or defects: Oxygen

Initial idea: **Incorporate Oxygen to getter radiation-induced vacancies**

⇒ **prevent formation of Di-vacancy ( $V_2$ ) related deep acceptor levels**

• Higher oxygen content ⇒ less negative space charge

One possible mechanism:  $V_2O$  is a deep acceptor



**DOFZ (Diffusion Oxygenated Float Zone Silicon) RD48 NIM A465 (2001) 60**

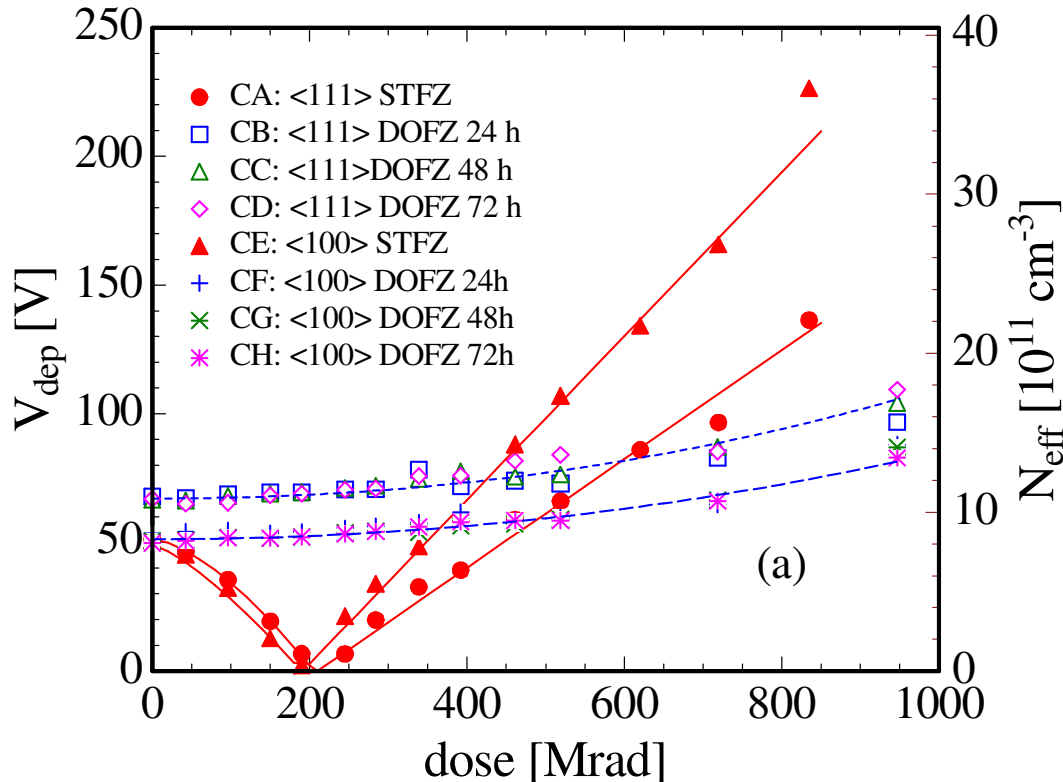
## Different kind of Si materials investigated by RD50

Material	Symbol	$\rho \Omega \text{ cm}$	$[\text{O}_i] \text{ cm}^{-3}$
Standard n-and p-type FZ	STNFZ	$1-7 \cdot 10^3$	$< 5 \cdot 10^{16}$
Diffusion Oxygenated FZ p and ntype	DOFZ	$1-7 \cdot 10^3$	$\sim 1-2 \cdot 10^{17}$
Epi-layer 50 $\mu\text{m}$ on CZ n-type ITME	EPI	50-100	substrate: $1 \cdot 10^{18}$
Czochralski Sumitomo, Japan n-type	CZ	$1.2 \cdot 10^3$	$\sim 8-9 \cdot 10^{17}$
Magnetic Czochralski Okmetic Finland n-type and p-type	MCZ	$1.2 \cdot 10^3$	$\sim 5-9 \cdot 10^{17}$

### Czochralski Si

- Very high Oxygen content  $10^{17}-10^{18}\text{cm}^{-3}$  (**Grown in  $\text{SiO}_2$  crucible**)
- High resistivity ( $>1\text{K}\Omega\text{cm}$ ) available only recently (**MCZ & CZ technology**)
- CZ wafers cheaper than FZ (**RF-IC industry got interested**)

# DOFZ Si: Spectacular Improvement of $\gamma$ -irradiation tolerance



Deep Levels responsible for macroscopic changes after  $\gamma$ -irradiation have been identified\*:

**I-defect**: acceptor level at  $E_C - 0.54\text{eV}$   
 (approx. 85% of damage in standard FZ)  
 → A candidate for the  $V_2O$  defect

**Bistable shallow thermal donor also important in oxygen enriched Si**

\*I.Pintilie et al., App PhysLett,82, 2169, (2003)

- No type inversion for oxygen enriched silicon!
- Slight increase of positive space charge
- Leakage increase not linear and depending on oxygen concentration

[E.Fretwurst et al. 1<sup>st</sup> RD50 Workshop]

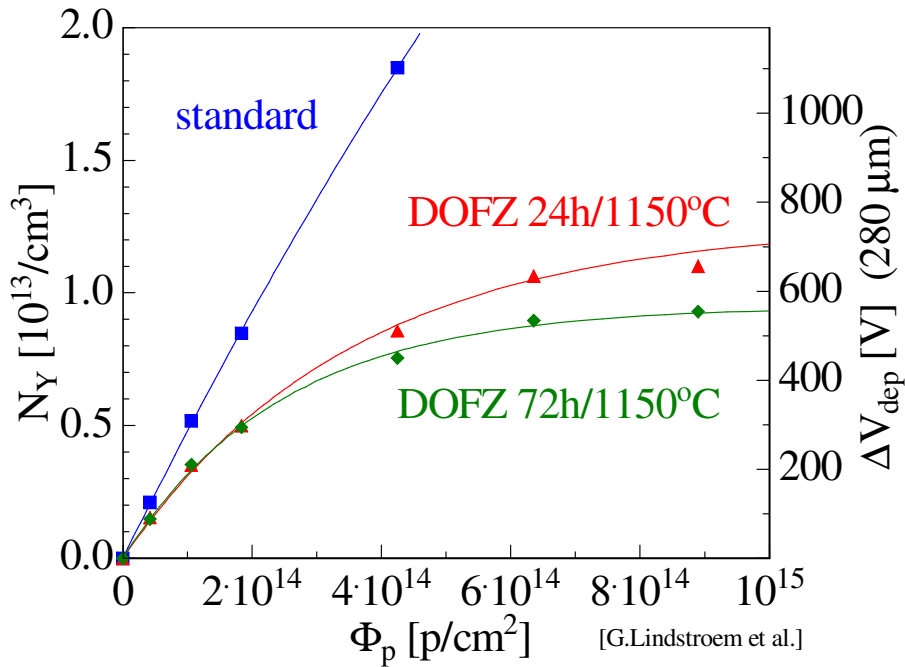
See also:

- Z.Li et al. [NIMA461(2001)126]

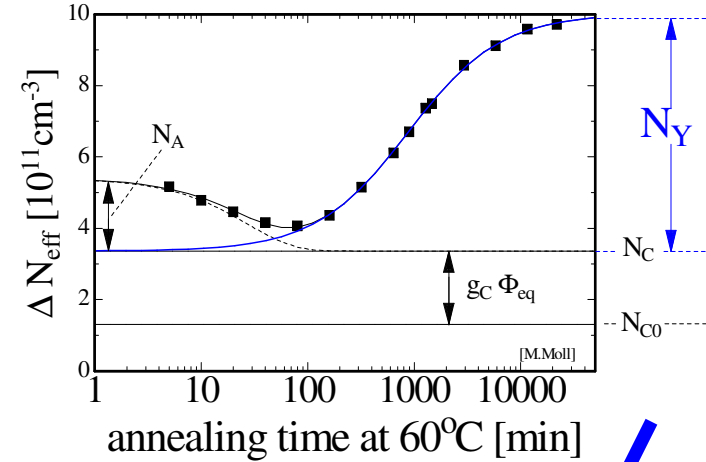
- Z.Li et al. [1<sup>st</sup> RD50 Workshop]

# DOFZ Si Reverse annealing: saturation of amplitude and time constant linearly increasing with fluence

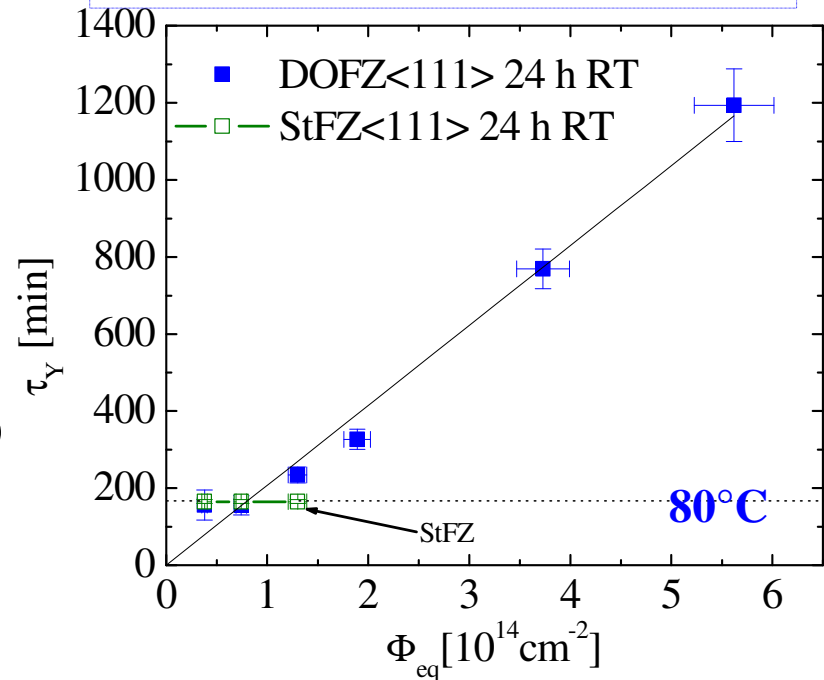
Data From G.Lindstrom et al.



Saturation of amplitude



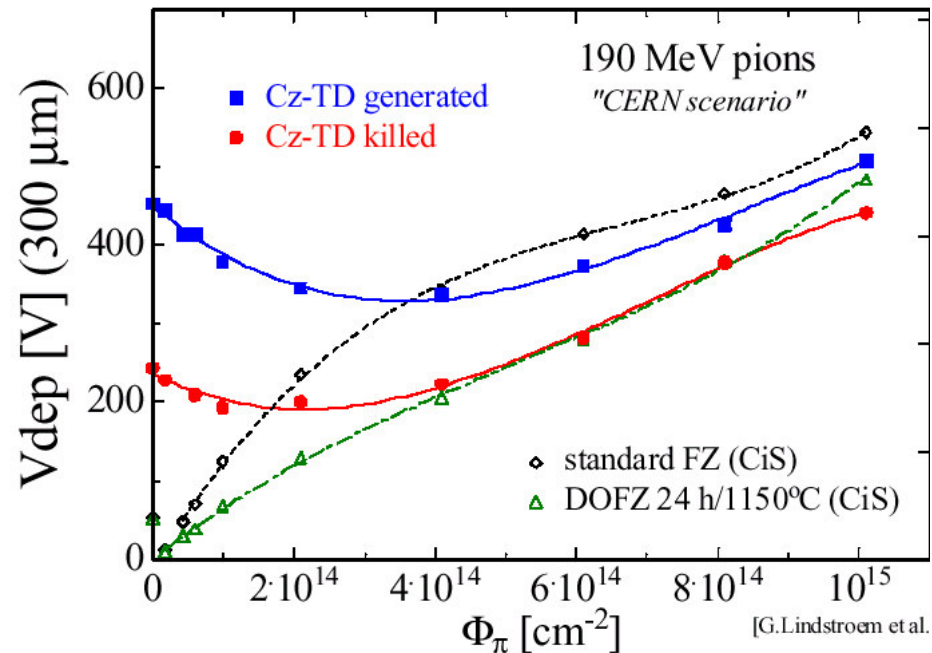
delayed reverse annealing



- Saturation of reverse annealing  
(24 GeV/c p - only little effect after neutron irradiation observed !)
- No big difference between  
24h and 72h oxidation at 1150°C
- time constant depending on fluence

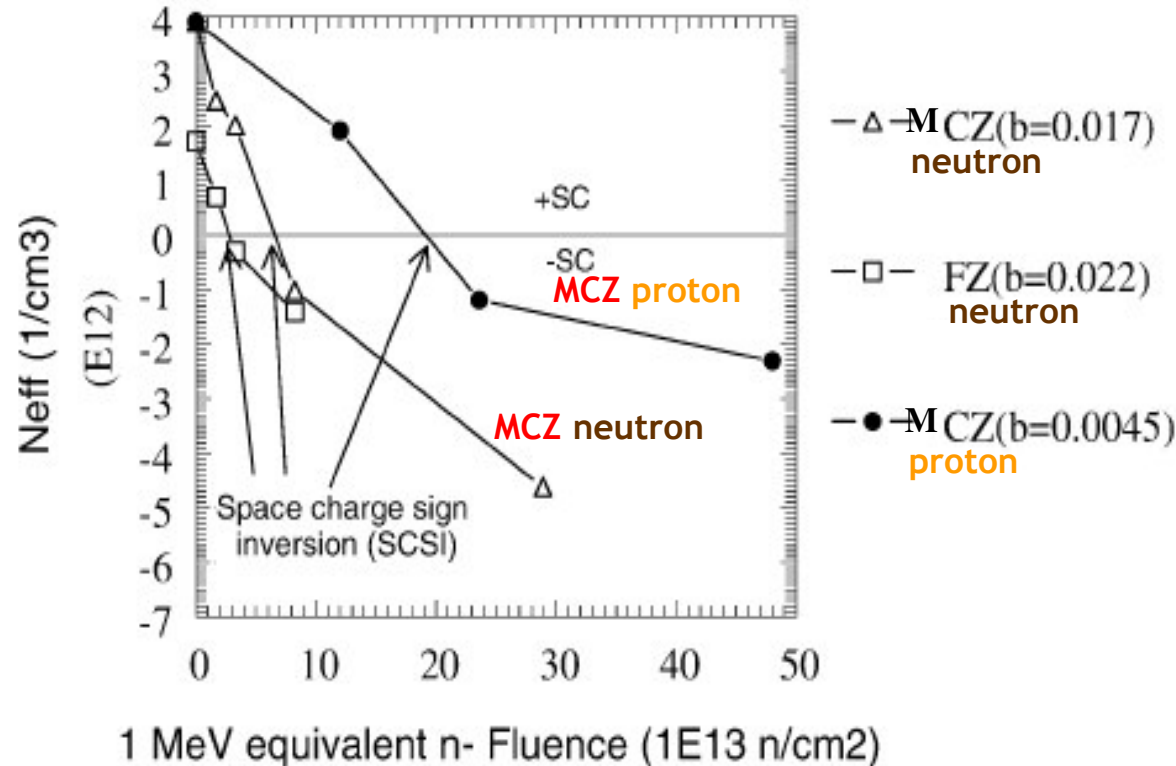
# Czochralski Si

**190 MeV  $\pi$  irradiation Villigen  
Cz from Sumitomo Sitix, Japan**



Data From G.Lindstrom et al.

**MCZ Okmetic after 24GeV/c p  
and neutron irradiation**



Data From Z. Li et al. IEEE TNS

◆ No or delayed type inversion (SCSI)

◆ Leakage current and charge trapping comparable to FZ silicon

## 5. Planar devices with defect engineered Si

### Activities in progress in RD50 on microstrip/pixel detectors

**Miniature microstrip detectors have been produced or are under process with FZ, DOFZ, MCZ, epitaxial n-type and p-type Si by:**

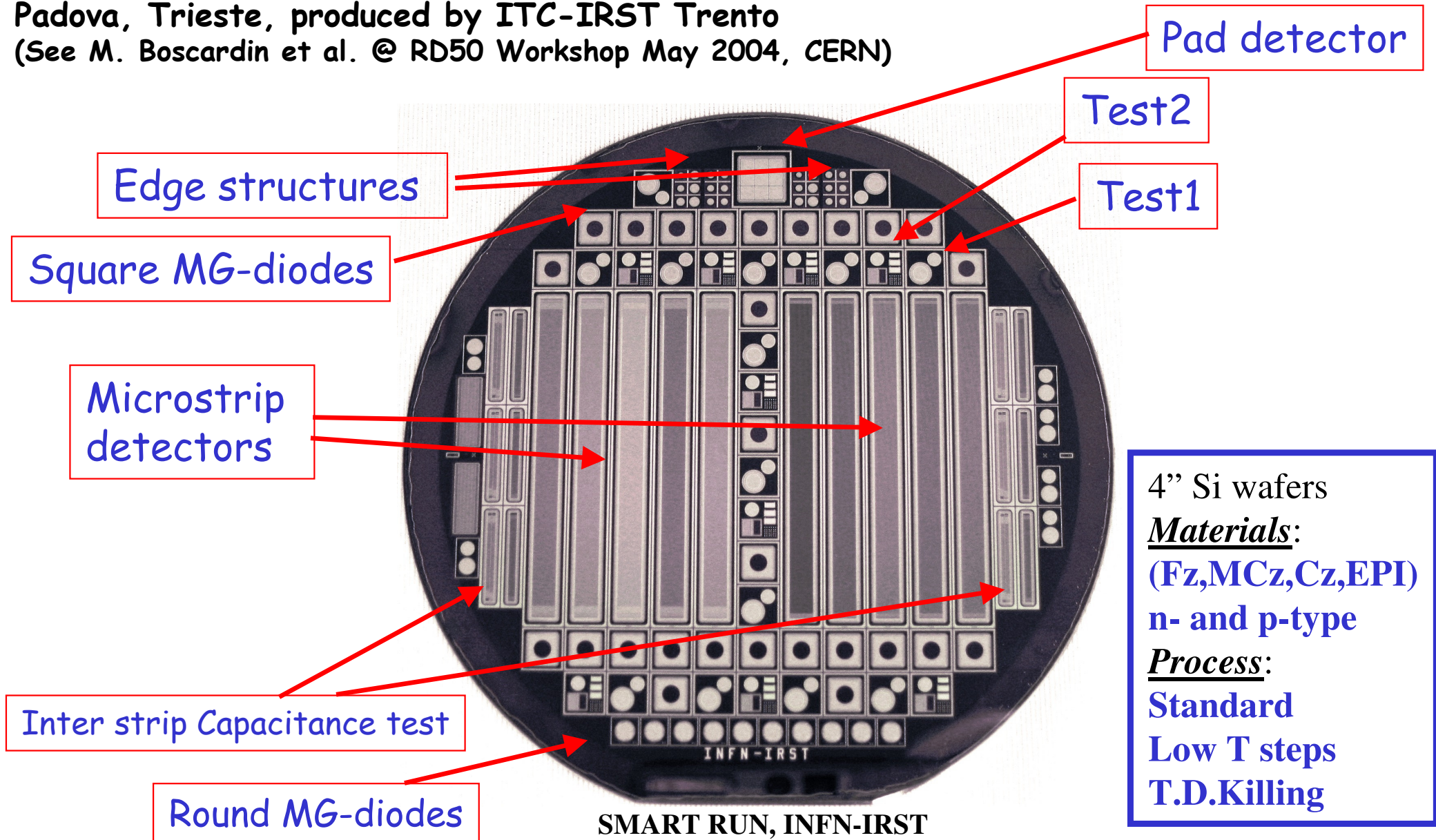
- CNM Barcelona & Liverpool
- IRST-Trento & italian groups (INFN)
- Helsinki HIP
- Common RD50 process under way

**Pixels are currently in process:**

- MCz, FZ, DOFZ n-type Si with Sintef using CMS/FPix masks (Purdue)
- MCz n-type Si with pixel and strips at BNL (Purdue, Rochester, BNL)
- CMOS Active Pixel Detectors (Perugia)

# DOFZ and MCZ, CZ, EPI microstrip detectors and other rad-hard test structures - Italy

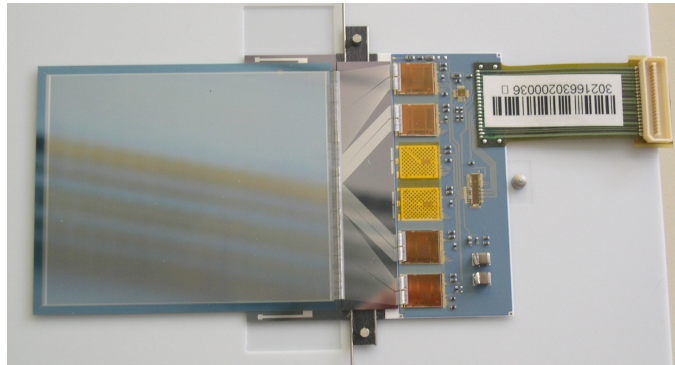
Italian network (INFN SMART project funded by CSN5): Pisa, Firenze, Bari, Perugia, Padova, Trieste, produced by ITC-IRST Trento  
(See M. Boscardin et al. @ RD50 Workshop May 2004, CERN)



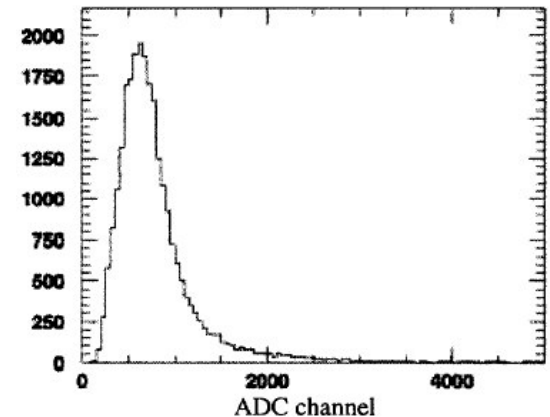


## MCZ Si microstrip detectors - Helsinki

A MCZ microstrip detector prototype (AC coupled, with 1024 strips, 6 cm long,  $w=10\ \mu\text{m}$ ,  $p=50\ \mu\text{m}$ ) has been tested by 225 GeV muon beam at CERN with AV1 chips

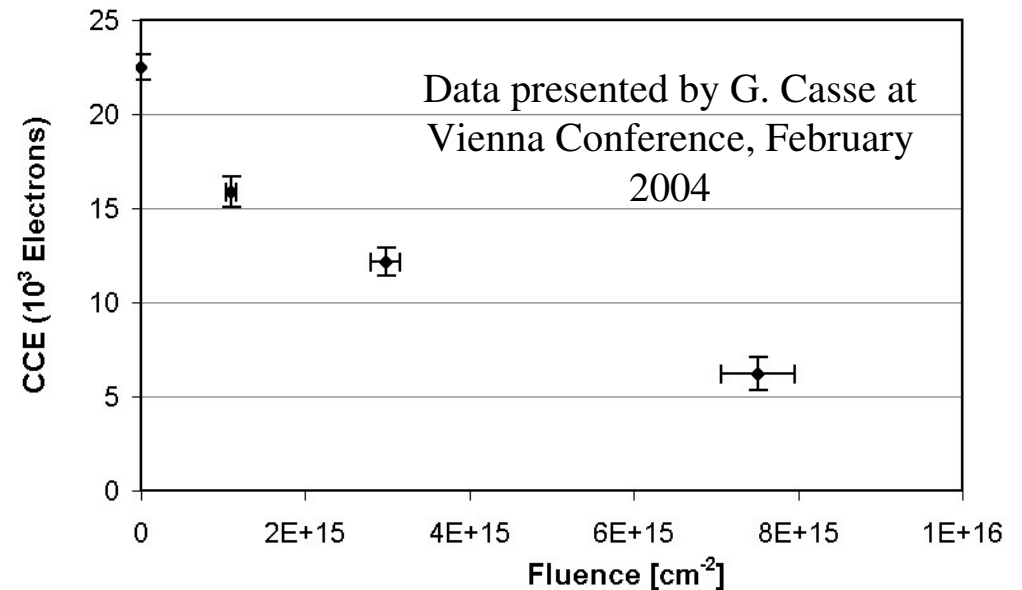


(E. Tuominen et al.,  
Nuclear Physics B, Proc.  
Suppl. 125 (2003) 175)



## n-on-p microstrip detectors – Liverpool & CNM Barcelona

- ❑ Material: standard p-type and oxygenated (DOFZ) p-type
- ❑ Detectors: miniature n-in-p microstrip (280 $\mu\text{m}$  thick).
- ❑ Electronics: SCT128A LHC speed (40MHz) chip.
- ❑ Irradiation: 24GeV p up to  $7.5 \cdot 10^{15}\ \text{cm}^{-2}$
- ❑ At highest fluence collected charge is  $Q \sim 6500e$  at  $V_{\text{bias}} = 900\text{V}$ . Corresponds to:  $\text{ccd} \sim 90\mu\text{m}$



## 6. Device Engineering

### Thin Si detectors

At S-LHC fluences, Charge collection limited by reduced carrier lifetimes  $\tau$ , due to trapping, carrier mean free path is less than the active thickness

- **Benefits:**

1. Smaller leakage current:  $I_{\text{leak}} \propto W$

2. Smaller depletion voltage  $V_{\text{dep}} = qW^2N_{\text{eff}}/2\epsilon \propto W^2$  and Space Charge Sign Inversion (SCSI) moved to higher fluences.

- **Drawbacks:**

- Lower mip signal in low fluence range, need low noise read-out

Group	$\beta_e$	$\beta_h$	particle	max $\Phi_{\text{eq}}$	T
	[ $10^{-16}\text{cm}^2\text{ns}^{-1}$ ]		/ energy	[ $\text{cm}^{-2}$ ]	[ $^{\circ}\text{C}$ ]
Ljubljana	4.1	6	reactor n	$10^{17}$	-10
	5.7	7.7	$\pi$		-10
	5.6	7.7	24 GeV/c p		-10
Hamburg	4.7	5.7	24 GeV/c p	$6 \cdot 10^{14}$	+20
Dortmund	5.1	5	24 GeV/c p	$10^{15}$	0

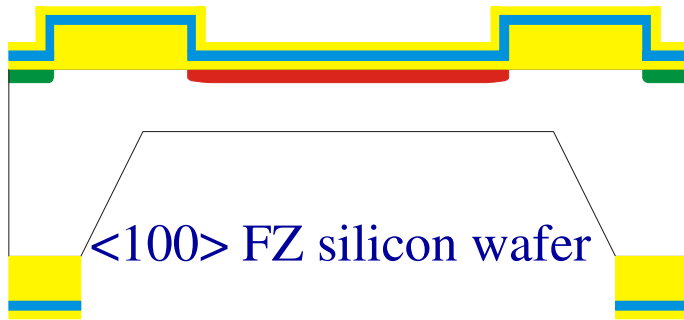
$$1/\tau_{e,h} = \beta_{e,h} \cdot \Phi_{\text{eq}} [\text{cm}^{-2}] \rightarrow \tau \sim 1/\Phi \quad \tau \sim 0.2 \text{ ns for } \Phi = 10^{16} \text{ cm}^{-2}$$

**No significant material difference FZ, DOFZ, MCZ**

# Thinned devices ( $W=50-100-50-200\mu\text{m}$ )

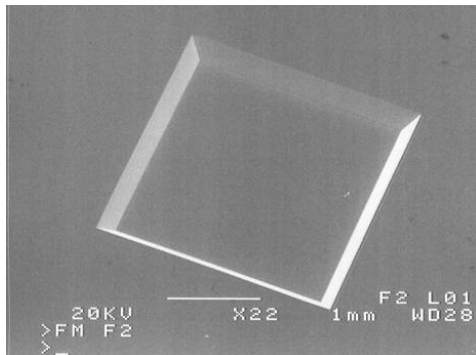
## ITC-IRST, Trento

(E. Ronchin et al., NIM A 530 (2004) 134)



1. TMAH etching from back.
2. Phosphorous deposition and diffusion from back.
3. Metal deposition from back.

Areas:  $1\text{ mm}^2 - 20\text{ mm}^2$  and  $I < 1\text{ nA/cm}^2$  at  $20\text{ V}$   
 Thickness:  $50-100\mu\text{m}$



SEM: back view of a thinned device

## Semiconductor Detector Laboratory, MPI, Munich

(L. Andricek et al., 1<sup>st</sup> ECFA Workshop, Montpellier, November 2003)

a) oxidation and back side implant of top wafer



b) wafer bonding and grinding/polishing of top wafer

c) process  $\rightarrow$  passivation

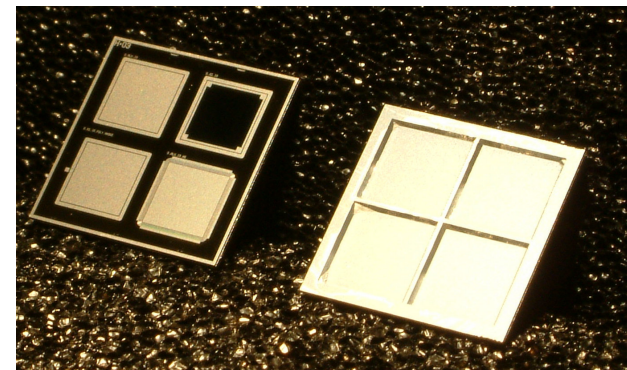


open backside passivation



d) anisotropic deep etching opens "windows" in handle wafer

Area:  $10\text{ mm}^2$  and  $I < 1\text{ nA/cm}^2$  at  $20\text{ V}$   
 Thickness:  $50\mu\text{m}$



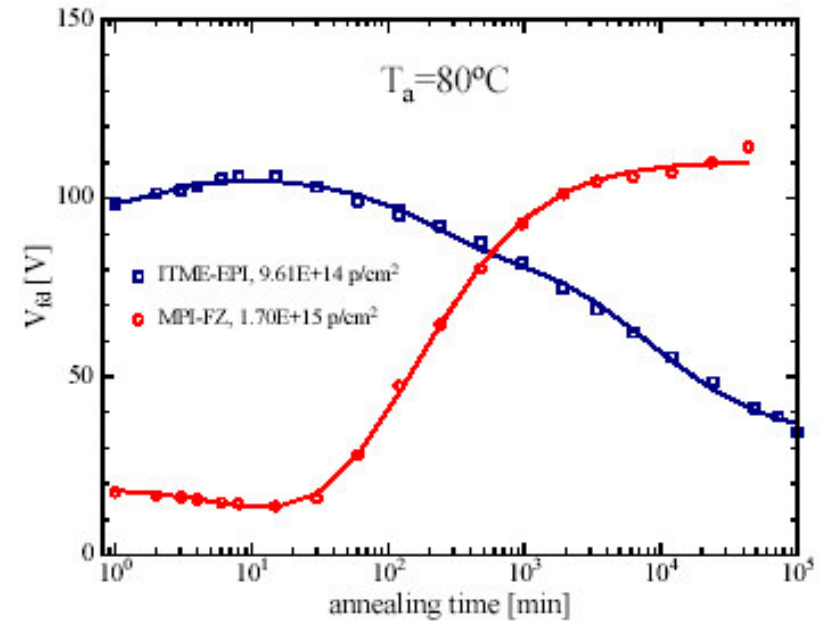
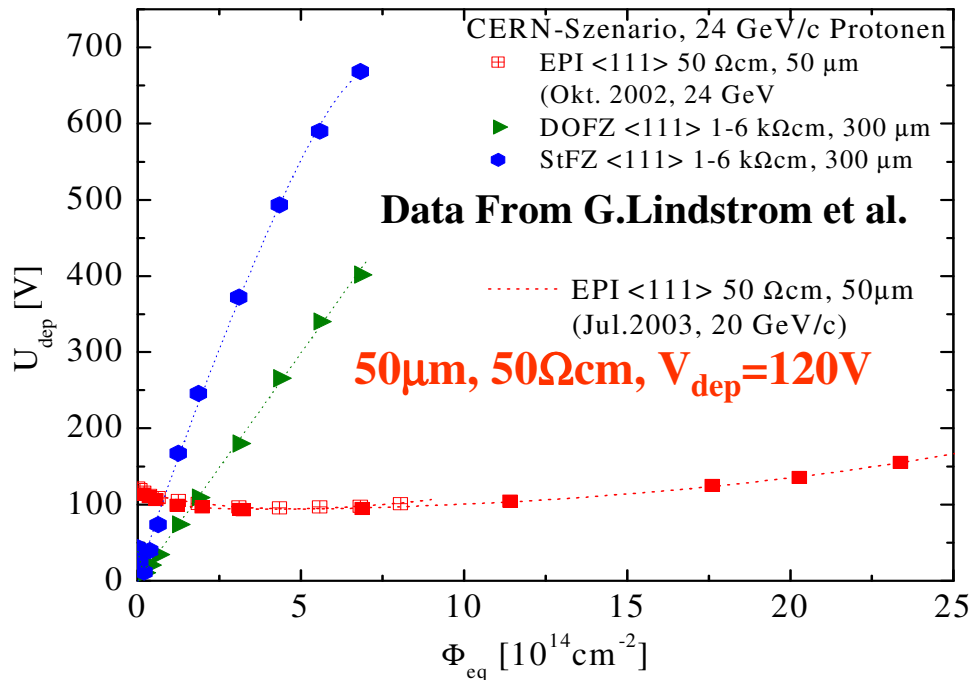
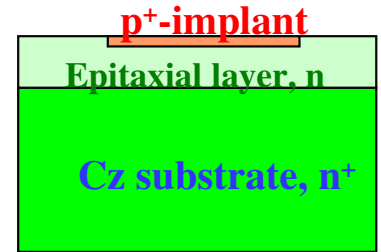
Photograph: front (left) and back (right) view of thinned devices

**More: Purdue is currently processing thin segmented detectors with Micron ( $150-200\mu\text{m}$ )**

# Epitaxial Si detectors

(Hamburg Group, NIM A 515 (2003) 665)

1. Substrate **low  $\rho$**  ( $<0.02 \Omega\cdot\text{cm}$ ) **300  $\mu\text{m}$  thick n-type Cz Si** Sb-doped.
  2. Thin (**25-50-75  $\mu\text{m}$** ) low  $\rho$  (**50  $\Omega\cdot\text{cm}$** ), P-doped **Si epilayer** grown by ITME (Warsaw)
  3.  $p^+$ -n by **B implant** on epilayer by CiS (Erfurt).
- Detector max active thickness equals the epitaxial layer thickness.



E. Fretwurst et al., 4th RD50 Workshop, 4-7 May 2004

## Advantages

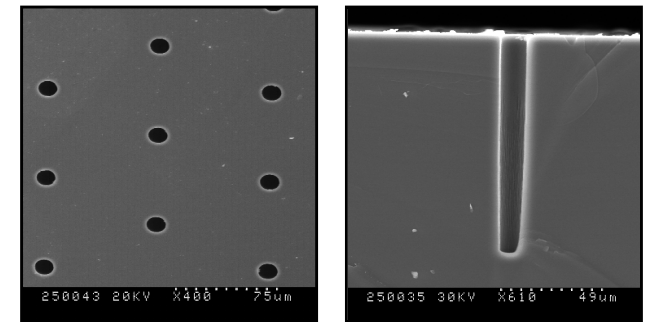
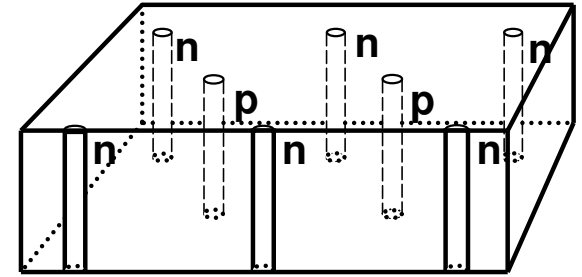
1. The **CZ substrate** is the backside  $n^+$  ohmic contact, which is not depleted due to the high Sb doping level and acts also as a **mechanical support** for the thin epitaxial detector.
2. Due to the high oxygen concentration in the CZ substrate ( $[O] \approx 10^{18} \text{ cm}^{-3}$ ), **O diffuses into the epitaxial layer** during the high temperature growth process **improving the detector radiation hardness**.
3. Different annealing behaviour in epitaxial Si  $\rightarrow$  No need of low temperature in maintenance operation of SLHC

# 3D detectors

First proposed by Sherwood Parker

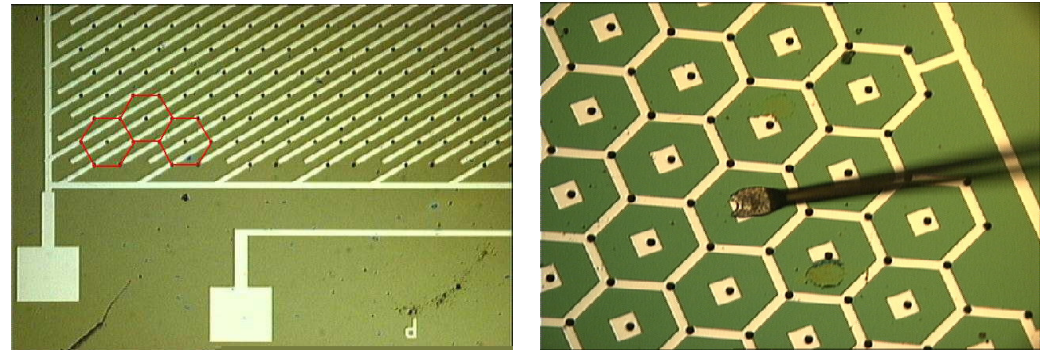
- Electrodes:
  - narrow columns along detector thickness-“3D”
  - diameter:  $10\mu\text{m}$  distance:  $50 - 100\mu\text{m}$
- Lateral depletion:
  - lower depletion voltage needed
  - thicker detectors possible
  - fast signal
- Hole processing :
  - Dry etching, Laser drilling, Photo Electro Chemical
  - Present aspect ratio (RD50) 13:1, Target: 30:1
- Electrode material
  - Doped Polysilicon

Present size  
up to  $\sim 1\text{cm}^2$



SEM and photos by Glasgow group

**3D hexagonal  
geometry connected  
in strip and pixel  
configurations**



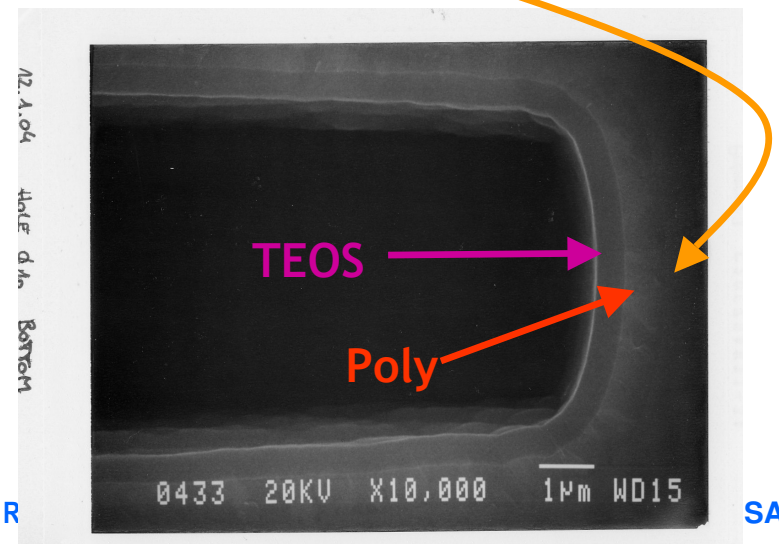
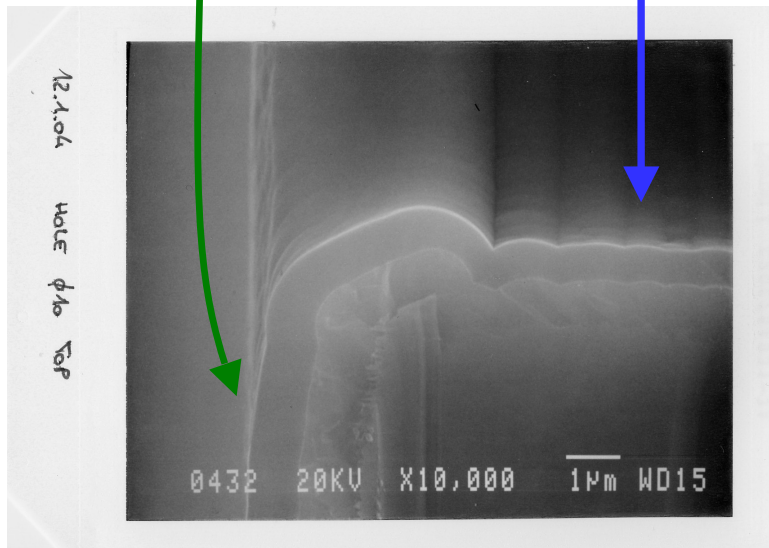
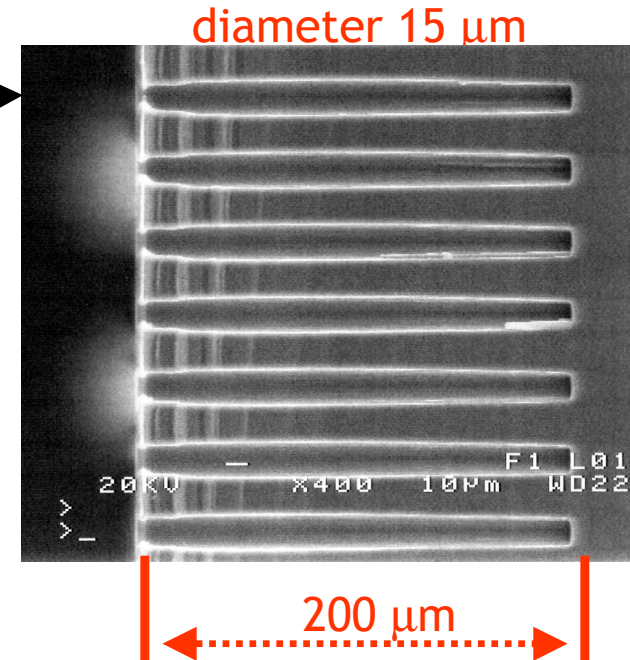
# 3-D detectors in RD50

(M. Boscardin et al., 4<sup>th</sup> RD50 Workshop, <http://rd50.web.cern.ch/rd50/4th-workshop>)

- Detector masks: Glasgow University
- Deep reaction ion etching: CNM, Barcelona
- Detector processing: IRST, Trento



	Surface	Top	Bottom
Poly	1.05 $\mu\text{m}$	0.8 $\mu\text{m}$	0.7 $\mu\text{m}$
TEOS	0.96 $\mu\text{m}$	0.7 $\mu\text{m}$	0.6 $\mu\text{m}$



Proposed by Z. Li, BNL Instr. Div. (NIM A 478 (2002) 303).

## Semi-3D detectors

Single-side detectors with alternative p- and n- strips on the front side.

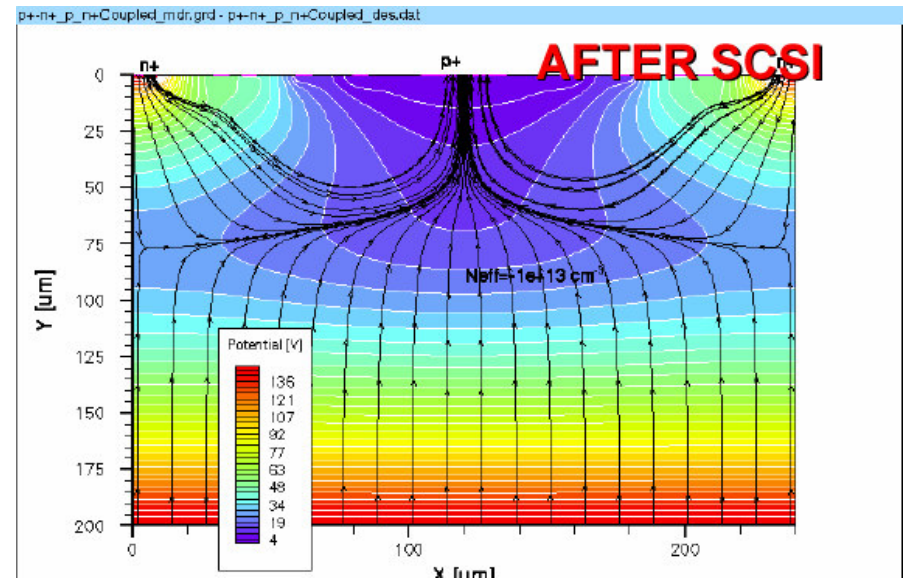
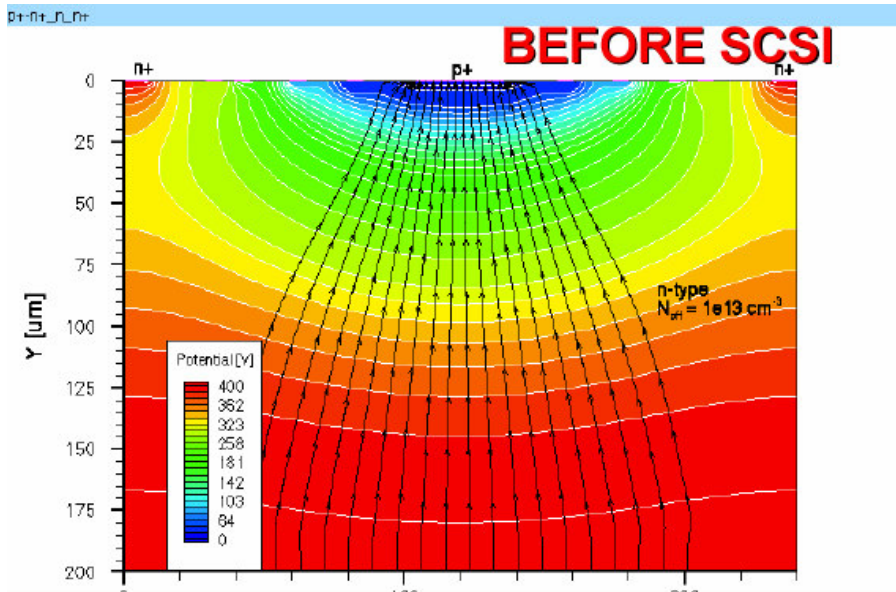
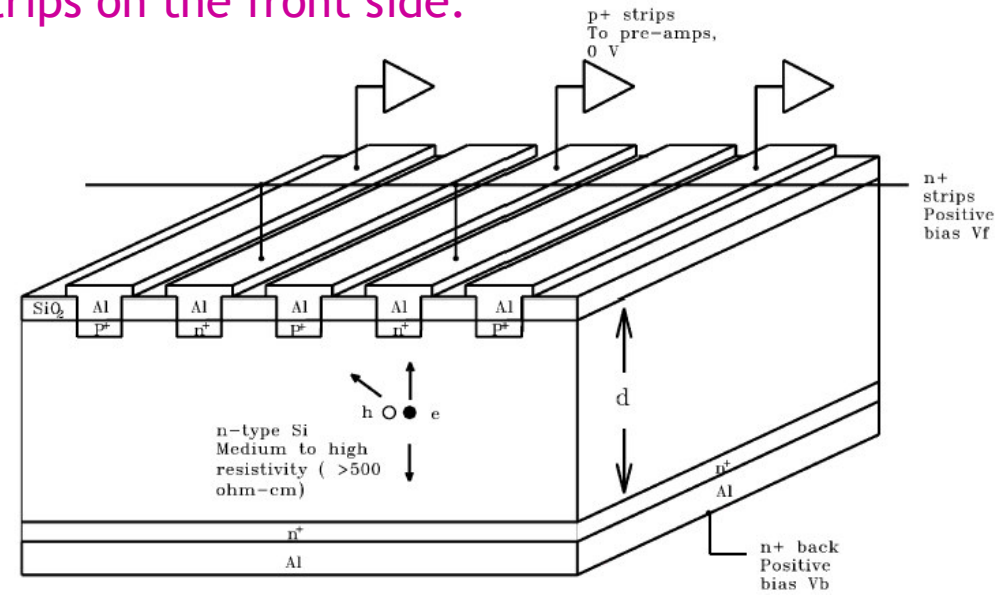
Activity of BNL & US Groups

### Advantages:

1. Single-side detector process.
2. Depletion occurs from both sides after SCSII reducing the depletion voltage by factor 2.5.

### Under investigation:

Electric field distribution before and after SCSII



(Z. Li and D. Bortoletto, 4<sup>th</sup> RD50 Workshop, <http://rd50.web.cern.ch/rd50/4th-workshop>)

Mara Bruzzi for the RD50 Collaboration, September 16, LECC2004, Boston, USA

Proposed by Z. Li, BNL Instr. Div. (NIM A 518 (2004) 738).

# Stripixel detectors

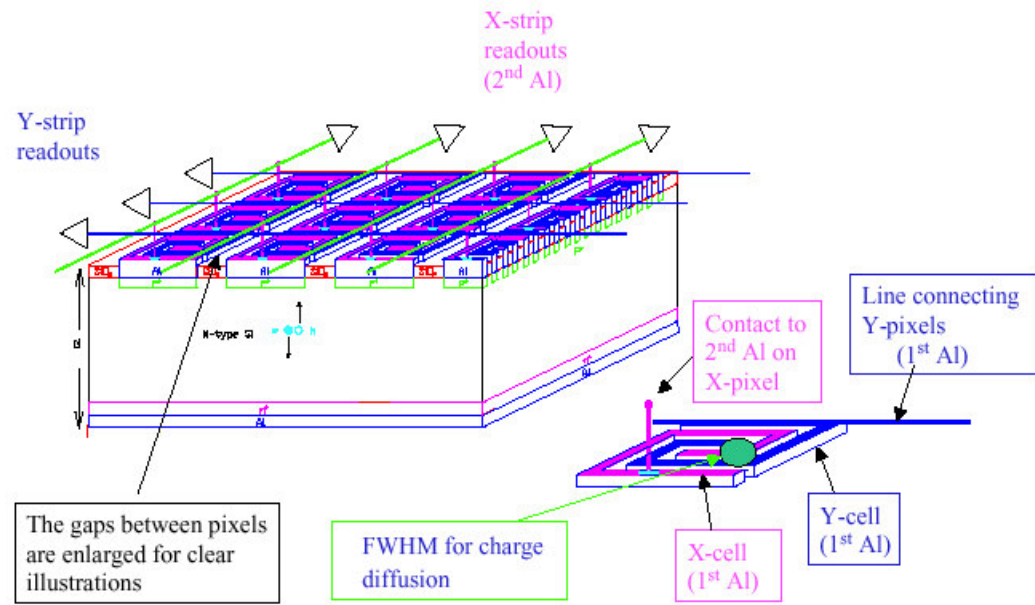
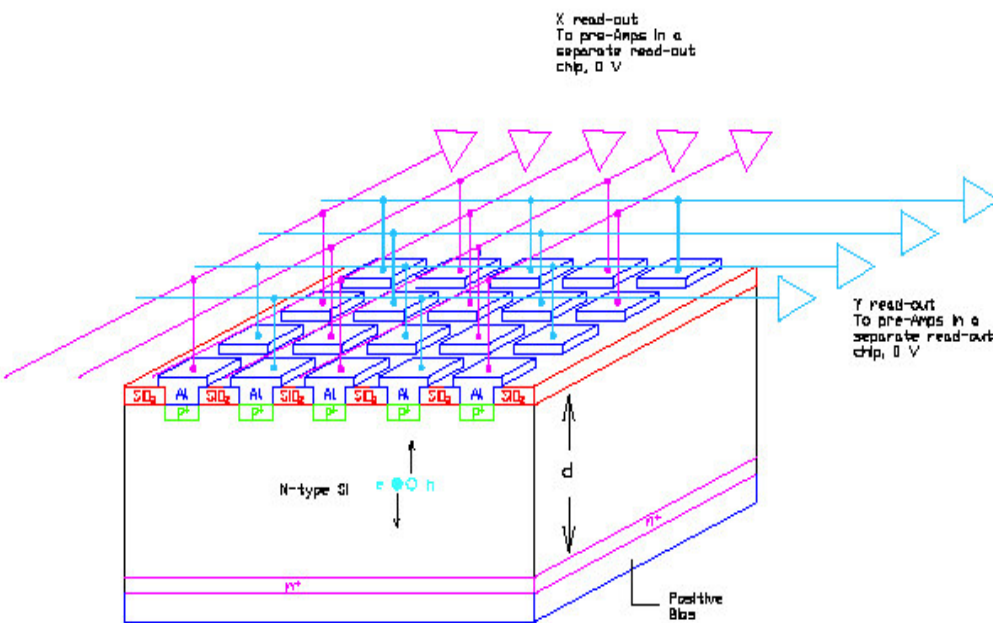
Pixel electrodes arranged in a projective X-Y readout.

## Advantages:

1. Projective readout of double-sided **strip** detectors minimizing the read-out channels;
2. Two-dimensional position resolution of **pixel** electrode geometry;
3. Single-side detector process with double metal technology;
4. Key parameter: standard deviation of the collected charge distribution:  $\chi$  ( $\approx 10 \mu\text{m}$ )
5. Alternative to macro-pixel detectors in the SLHC upgrade between 15 cm and 60 cm?

- Individual pixels alternatively connected to X- & Y- read-out
- Charge must be collected at least by two pixels.
- Key condition:  $\chi \geq \text{pitch}$
- Resolution can be better than pitch.

- Each pixel is divided in two parts (X- & Y-cell).
- Charge must be collected at least by 1 X- & 1 Y-cells.
- Key:  $\chi \geq \text{interleaved distance between X- & Y- cells}$
- If  $\text{pitch} > \chi$  the resolution is fixed by the pitch.





## 7. Summary

- Radiation hard materials for tracker detectors at SuperLHC are under study by the CERN RD50 collaboration.
- At the fluence of  $10^{16}\text{cm}^{-2}$  (Innermost layer of a SLHC detector) the active thickness of any silicon material is significantly reduced due to trapping. The two most promising options so far are:

**Thin detectors : drawback: radiation hard electronics for low signals needed**

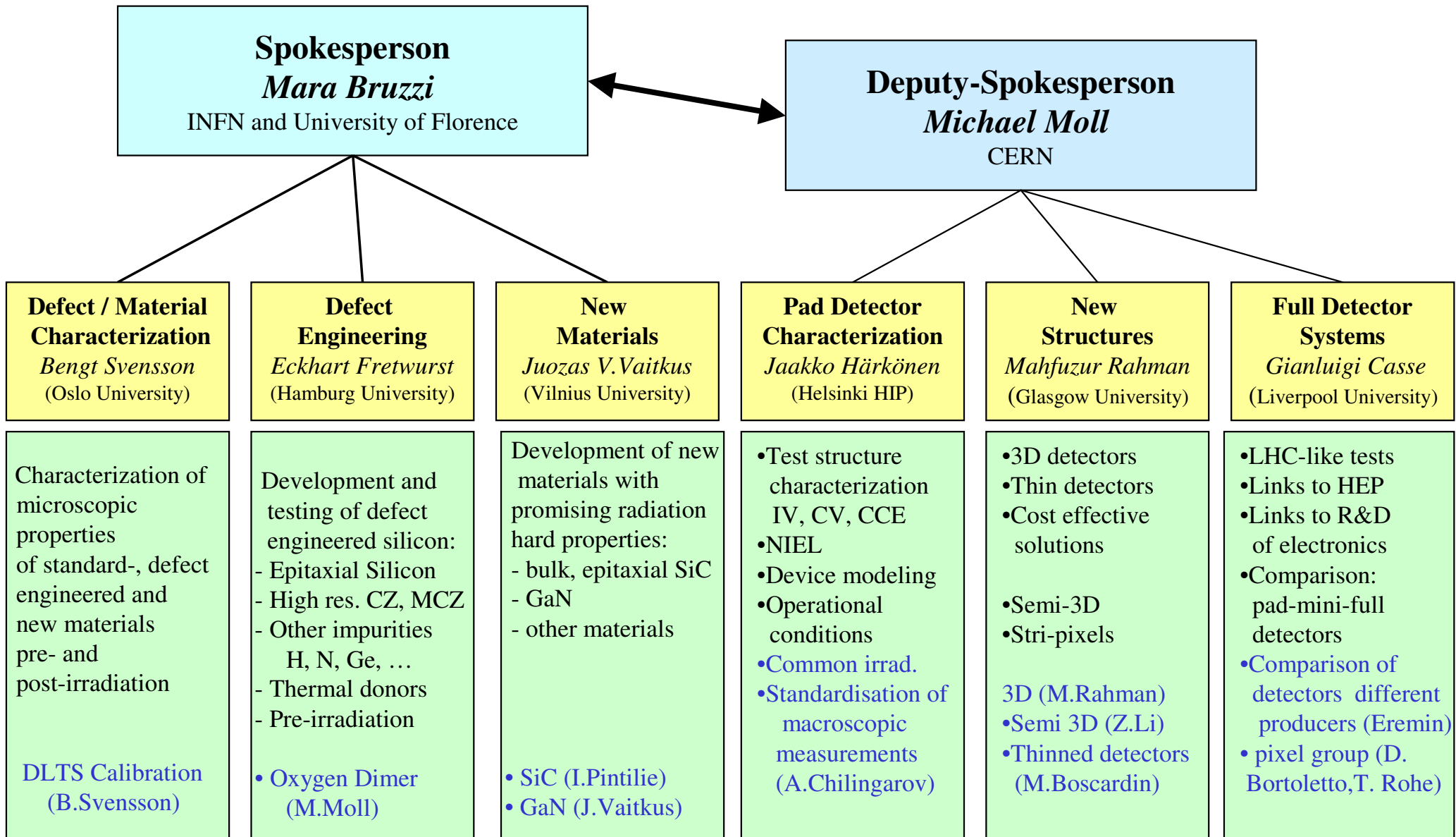
**3D detectors: drawback: technology has to be optimized**

- At fluences up to  $10^{15}\text{cm}^{-2}$  (Outer layers of a SLHC detector) the change of the depletion voltage and the large area to be covered by detectors is the major problem. **CZ detectors could be a cost-effective radiation hard solution**
- Miniature microstrip and pixel detectors made with defect engineered Si have been fabricated by RD50 or are in process. First studies after irradiation with LHC-like electronics are encouraging: CCE on microstrip n-in-p oxygenated detector irradiated up to  $7 \times 10^{15}$  [ $24\text{GeVp}/\text{cm}^2$ ] is  $> 6500\text{e}$ .
- New Materials like SiC, GaN are under investigation. More radiation studies needed.

**RD50 web-site: <http://www.cern.ch/rd50>**

# Scientific Organization of RD50

*Development of Radiation Hard Semiconductor Devices for High Luminosity Colliders*



# RD50 Primary Damage and secondary defect formation

- Two basic defects

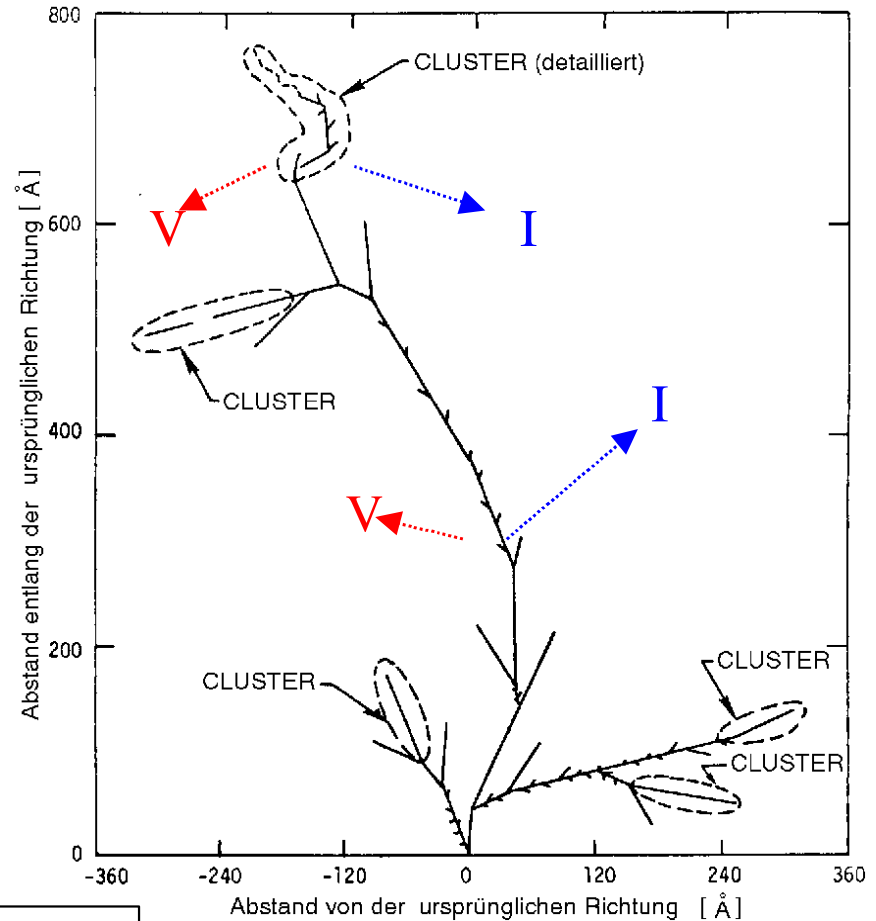
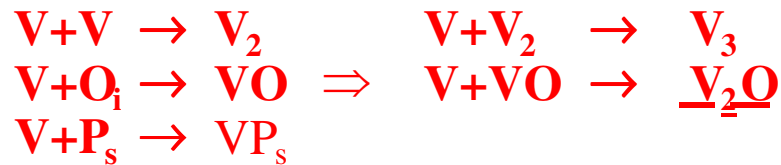
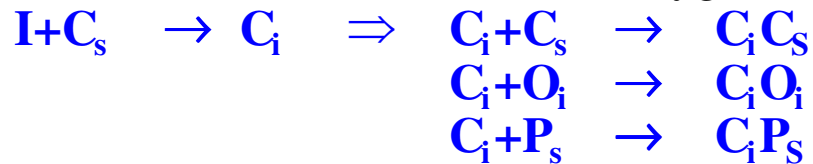
I - Silicon Interstitial      V - Vacancy

- Primary defect generation

I, I<sub>2</sub> higher order I (?)  
 ⇒ I-CLUSTER (?) ← Damage?!  
 V, V<sub>2</sub>, higher order V (?)  
 ⇒ V-CLUSTER (?) ←

- Secondary defect generation

Main impurities in silicon: Carbon (C<sub>s</sub>)  
 Oxygen (O<sub>i</sub>)



Damage?! (“V<sub>2</sub>O-model”)

## B. New materials studied by RD50: SiC, GaN, ...

SiC  $E_g = 3.3\text{eV}$ , (CC mips =  $55\text{e}/\mu\text{m}$ ) ; GaN  $E_g = 3.39\text{eV}$

→ Low Leakage current even after very high fluences, radiation hardness under test

- **Semi-Insulating SiC**

- CCE 60% in as-grown CREE vanadium compensated, ~55% after irradiation with  $10^{13}\text{cm}^{-2}$  300 MeV/c  $\pi$

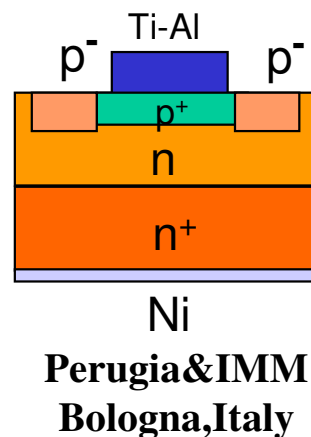
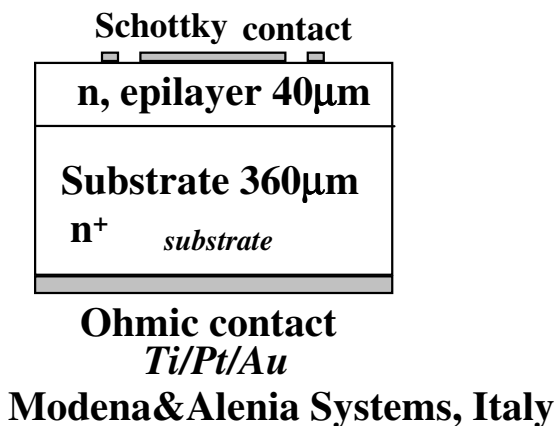
- Vanadium-free semi-insulating material from Okmetic: ~100% CCE in unirradiated material

- **Epitaxial SiC**

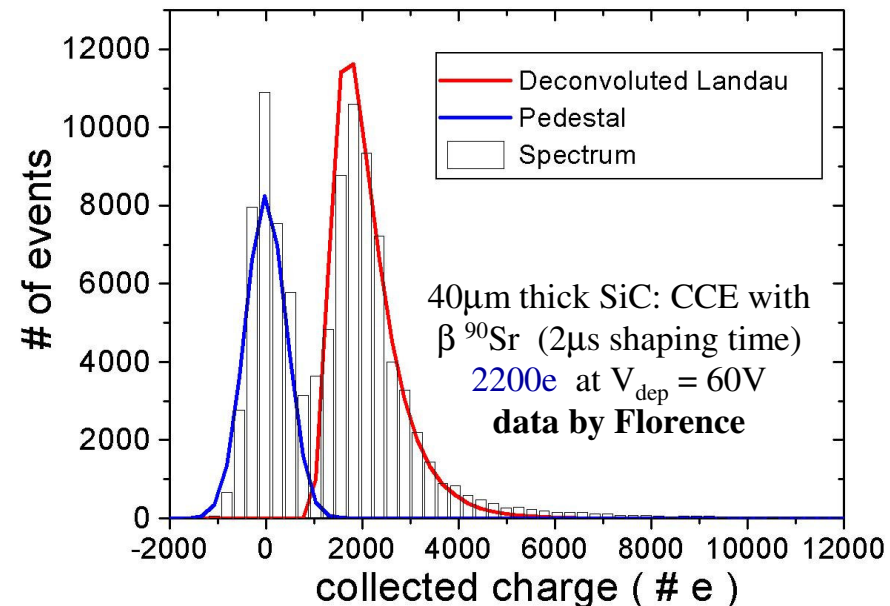
- 6 new 2" wafers  $d \sim 50\mu\text{m}$ ,  $N_{\text{eff}} \geq 5 \cdot 10^{13}\text{cm}^{-3}$  produced by IKZ, Berlin

- Common RD50 test structures produced and irradiated. Tests in progress

- 100% CCE in unirradiated material



Epilayer SiC from IKZ, Berlin  
Substrate SiC from Okmetic or CREE



F. Nava et al., IEEE Trans. Nucl. Sci, (2004)

# **RADIATION FACILITIES within RD50**

- **24 GeV/c protons, PS-CERN**  
up to  $10^{16}$  cm<sup>-2</sup>
- **TRIGA reactor neutrons, Ljubljana**  
up to  $1 \times 10^{16}$  cm<sup>-2</sup>
- **26 MeV protons, Karlsruhe**  
 $1 \times 10^{14}$ /cm<sup>2</sup> on  $10 \times 10$  cm<sup>2</sup> in 10 minutes
- **10-50 MeV protons, Jyvaskyla +Helsinki**  
up to  $3 \times 10^{14}$  cm<sup>-2</sup>
- **<sup>60</sup>Co dose, BNL, USA**  
up to 1.5GRad
- **58 MeV Li ions, Legnaro/ Padova**
- **900 MeV electrons, Trieste**
- **15MeV electrons, Oslo**